

8 7 6 5 4 3 2 1

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D
C
B
A

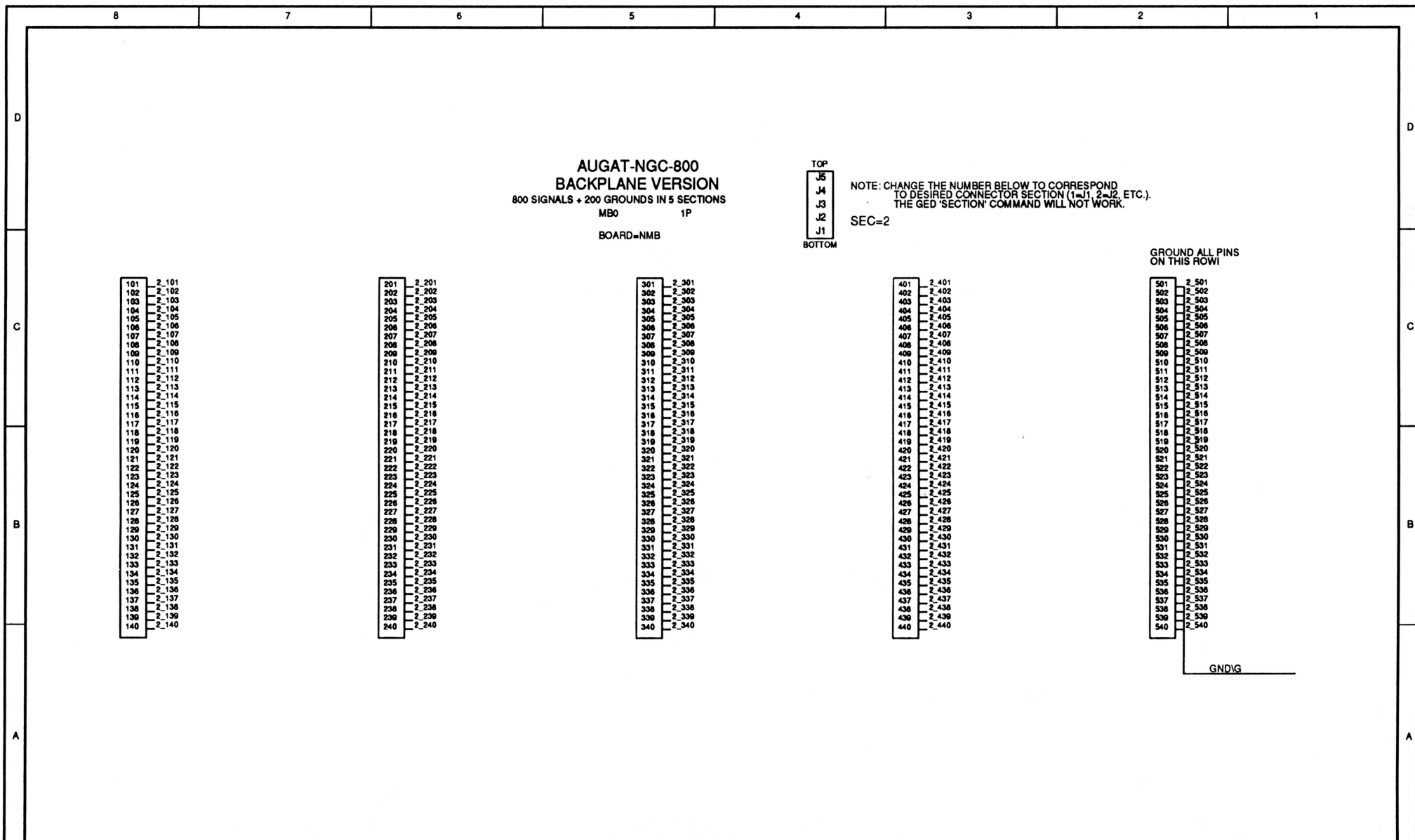
D
C
B
A

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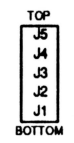


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8 7 6 3 2 1



**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
MBO 1P
BOARD=NMB



NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=2

GROUND ALL PINS ON THIS ROW!

- 101 2_101
- 102 2_102
- 103 2_103
- 104 2_104
- 105 2_105
- 106 2_106
- 107 2_107
- 108 2_108
- 109 2_109
- 110 2_110
- 111 2_111
- 112 2_112
- 113 2_113
- 114 2_114
- 115 2_115
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- 139 2_139
- 140 2_140

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- 240 2_240

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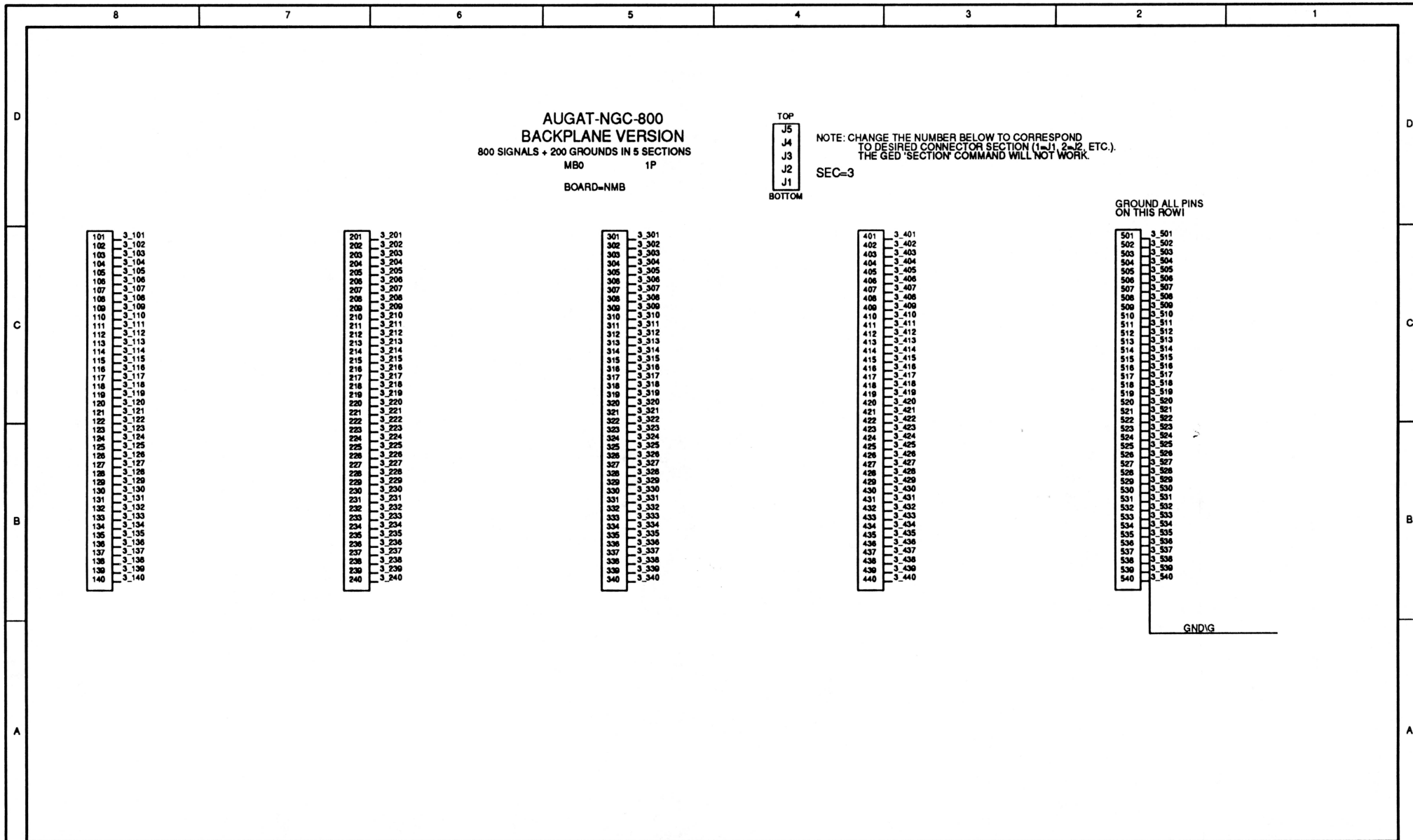
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- 536 2_536
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- 538 2_538
- 539 2_539
- 540 2_540

GND'G

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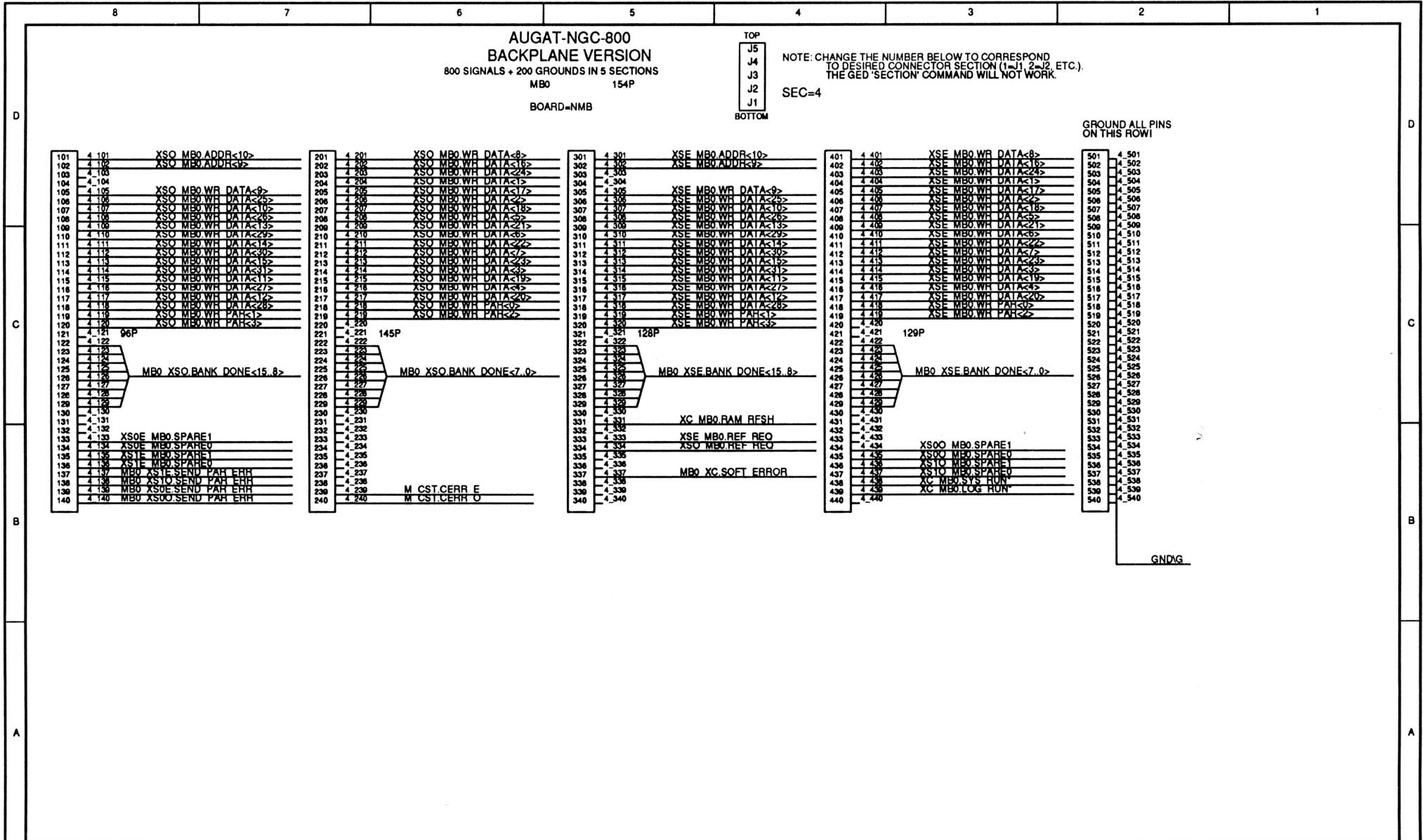
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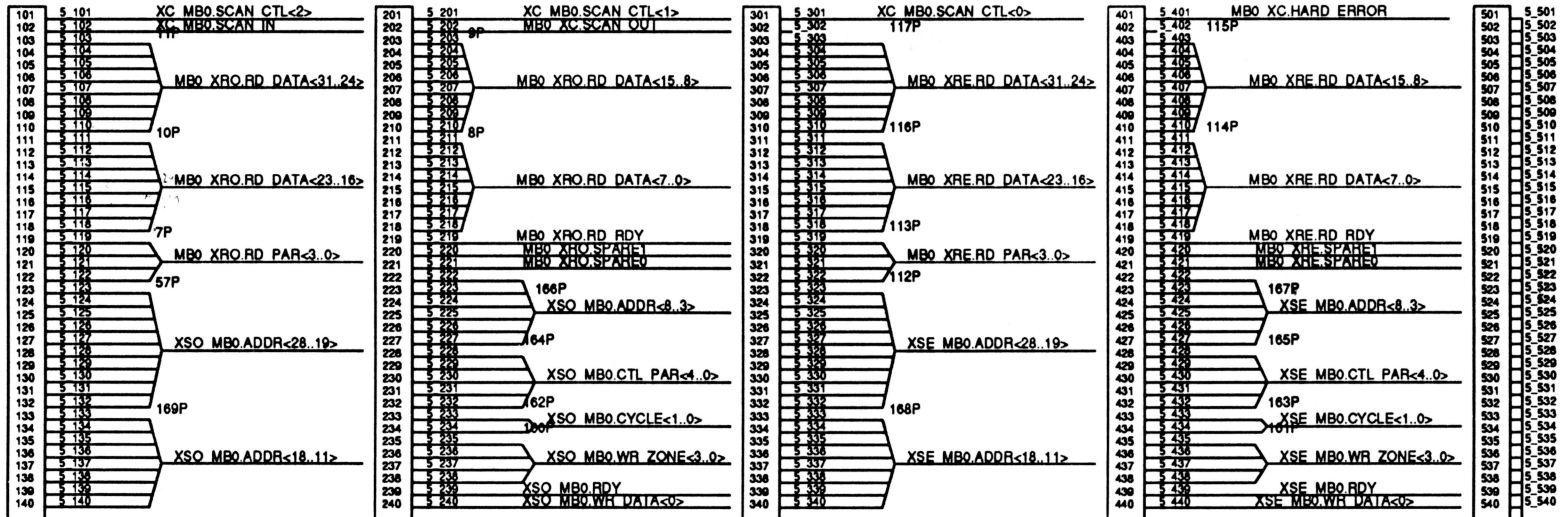
8 7 6 5 4 3 2 1

**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
MBO 174P
BOARD=NMB

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND
TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.).
THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=5

GROUND ALL PINS
ON THIS ROW!



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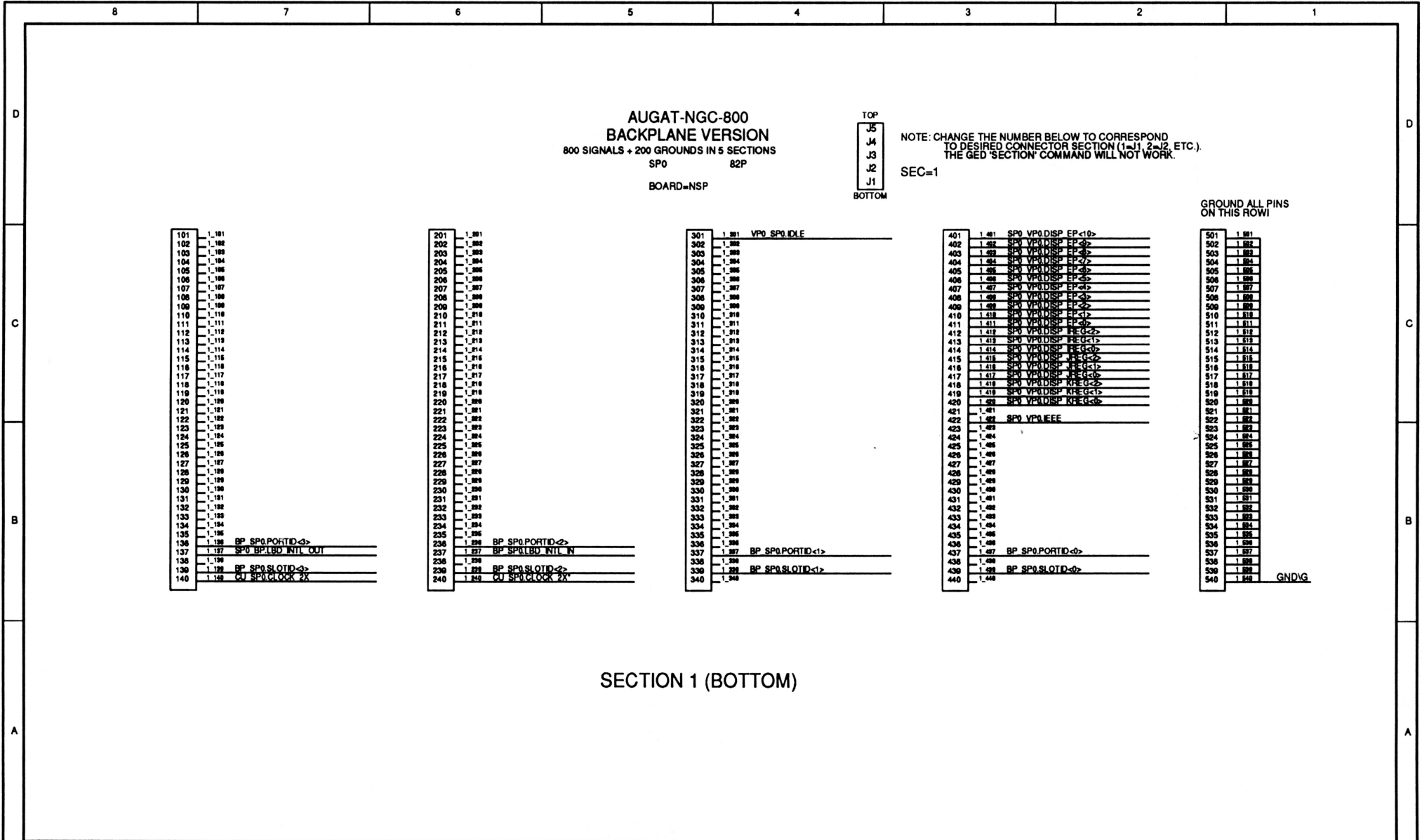


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8 7 6 3 2 1



**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
SP0 82P
BOARD=NSP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1-J1, 2-J2, ETC.). THE GED SECTION COMMAND WILL NOT WORK.
SEC=1

GROUND ALL PINS ON THIS ROW!

101	1_181	201	1_201	301	1_301	VP0 SP0 IDLE	401	1_401	SP0 VP0 DISP EP<1>	501	1_501
102	1_182	202	1_202	302	1_302		402	1_402	SP0 VP0 DISP EP<2>	502	1_502
103	1_183	203	1_203	303	1_303		403	1_403	SP0 VP0 DISP EP<3>	503	1_503
104	1_184	204	1_204	304	1_304		404	1_404	SP0 VP0 DISP EP<4>	504	1_504
105	1_185	205	1_205	305	1_305		405	1_405	SP0 VP0 DISP EP<5>	505	1_505
106	1_186	206	1_206	306	1_306		406	1_406	SP0 VP0 DISP EP<6>	506	1_506
107	1_187	207	1_207	307	1_307		407	1_407	SP0 VP0 DISP EP<7>	507	1_507
108	1_188	208	1_208	308	1_308		408	1_408	SP0 VP0 DISP EP<8>	508	1_508
109	1_189	209	1_209	309	1_309		409	1_409	SP0 VP0 DISP EP<9>	509	1_509
110	1_110	210	1_210	310	1_310		410	1_410	SP0 VP0 DISP EP<10>	510	1_510
111	1_111	211	1_211	311	1_311		411	1_411	SP0 VP0 DISP EP<11>	511	1_511
112	1_112	212	1_212	312	1_312		412	1_412	SP0 VP0 DISP EP<12>	512	1_512
113	1_113	213	1_213	313	1_313		413	1_413	SP0 VP0 DISP EP<13>	513	1_513
114	1_114	214	1_214	314	1_314		414	1_414	SP0 VP0 DISP EP<14>	514	1_514
115	1_115	215	1_215	315	1_315		415	1_415	SP0 VP0 DISP EP<15>	515	1_515
116	1_116	216	1_216	316	1_316		416	1_416	SP0 VP0 DISP EP<16>	516	1_516
117	1_117	217	1_217	317	1_317		417	1_417	SP0 VP0 DISP EP<17>	517	1_517
118	1_118	218	1_218	318	1_318		418	1_418	SP0 VP0 DISP EP<18>	518	1_518
119	1_119	219	1_219	319	1_319		419	1_419	SP0 VP0 DISP EP<19>	519	1_519
120	1_120	220	1_220	320	1_320		420	1_420	SP0 VP0 DISP EP<20>	520	1_520
121	1_121	221	1_221	321	1_321		421	1_421	SP0 VP0 DISP EP<21>	521	1_521
122	1_122	222	1_222	322	1_322		422	1_422	SP0 VP0 DISP EP<22>	522	1_522
123	1_123	223	1_223	323	1_323		423	1_423	SP0 VP0 DISP EP<23>	523	1_523
124	1_124	224	1_224	324	1_324		424	1_424	SP0 VP0 DISP EP<24>	524	1_524
125	1_125	225	1_225	325	1_325		425	1_425	SP0 VP0 DISP EP<25>	525	1_525
126	1_126	226	1_226	326	1_326		426	1_426	SP0 VP0 DISP EP<26>	526	1_526
127	1_127	227	1_227	327	1_327		427	1_427	SP0 VP0 DISP EP<27>	527	1_527
128	1_128	228	1_228	328	1_328		428	1_428	SP0 VP0 DISP EP<28>	528	1_528
129	1_129	229	1_229	329	1_329		429	1_429	SP0 VP0 DISP EP<29>	529	1_529
130	1_130	230	1_230	330	1_330		430	1_430	SP0 VP0 DISP EP<30>	530	1_530
131	1_131	231	1_231	331	1_331		431	1_431	SP0 VP0 DISP EP<31>	531	1_531
132	1_132	232	1_232	332	1_332		432	1_432	SP0 VP0 DISP EP<32>	532	1_532
133	1_133	233	1_233	333	1_333		433	1_433	SP0 VP0 DISP EP<33>	533	1_533
134	1_134	234	1_234	334	1_334		434	1_434	SP0 VP0 DISP EP<34>	534	1_534
135	1_135	235	1_235	335	1_335		435	1_435	SP0 VP0 DISP EP<35>	535	1_535
136	1_136	236	1_236	336	1_336		436	1_436	SP0 VP0 DISP EP<36>	536	1_536
137	1_137	237	1_237	337	1_337	BP SP0 PORTID<1>	437	1_437	BP SP0 PORTID<2>	537	1_537
138	1_138	238	1_238	338	1_338		438	1_438		538	1_538
139	1_139	239	1_239	339	1_339	BP SP0 SLOTTD<1>	439	1_439	BP SP0 SLOTTD<2>	539	1_539
140	1_140	240	1_240	340	1_340		440	1_440		540	1_540

SECTION 1 (BOTTOM)

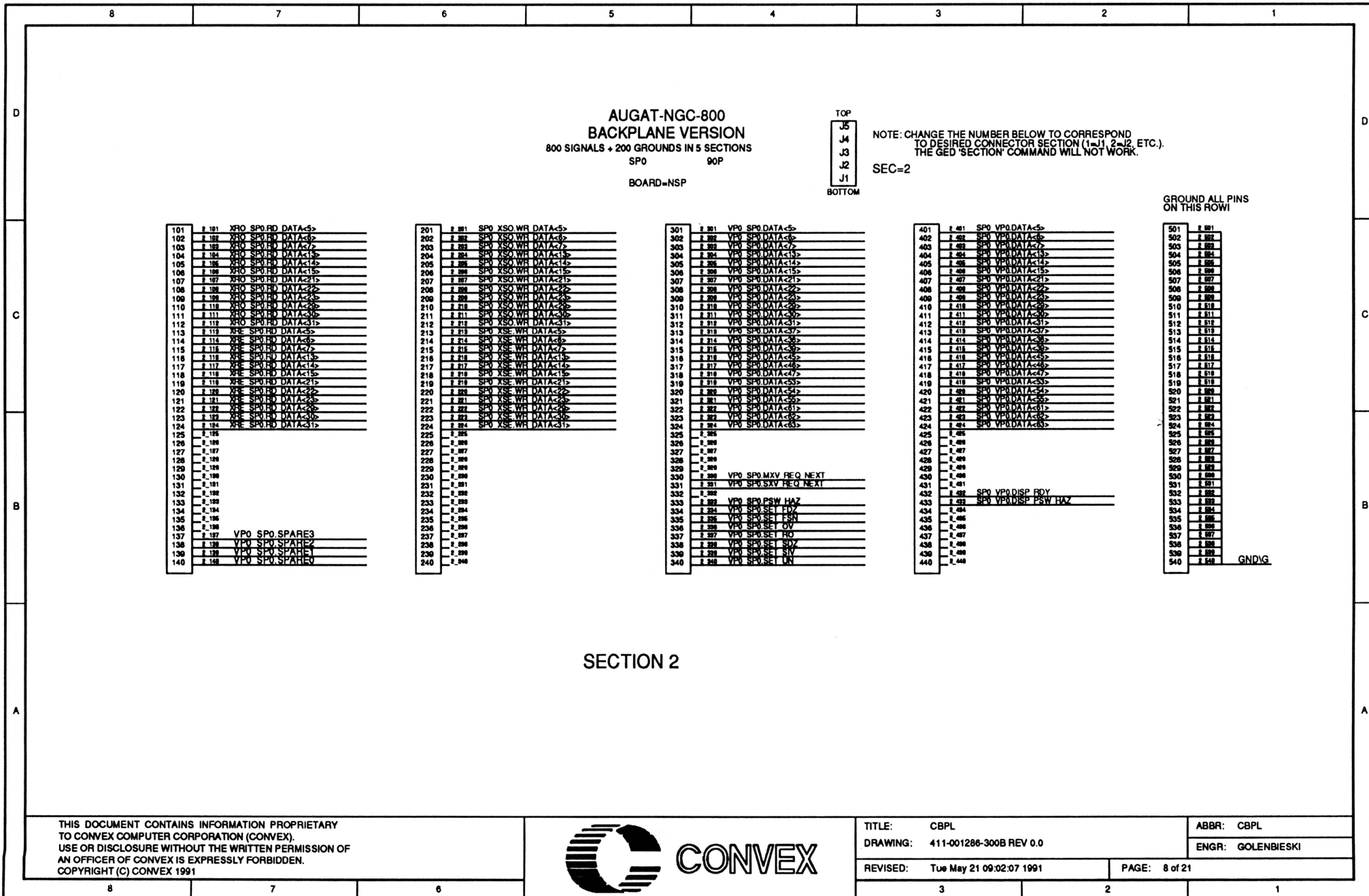
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**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
SPO 90P
BOARD=NSP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=2

GROUND ALL PINS ON THIS ROW

101	1 181	XRO SPO RD DATA<5>
102	1 182	XRO SPO RD DATA<6>
103	1 183	XRO SPO RD DATA<7>
104	1 184	XRO SPO RD DATA<13>
105	1 185	XRO SPO RD DATA<14>
106	1 186	XRO SPO RD DATA<15>
107	1 187	XRO SPO RD DATA<21>
108	1 188	XRO SPO RD DATA<22>
109	1 189	XRO SPO RD DATA<23>
110	1 119	XRO SPO RD DATA<28>
111	1 111	XRO SPO RD DATA<30>
112	1 112	XRO SPO RD DATA<31>
113	1 113	XRE SPO RD DATA<5>
114	1 114	XRE SPO RD DATA<6>
115	1 115	XRE SPO RD DATA<7>
116	1 116	XRE SPO RD DATA<13>
117	1 117	XRE SPO RD DATA<14>
118	1 118	XRE SPO RD DATA<15>
119	1 119	XRE SPO RD DATA<21>
120	1 120	XRE SPO RD DATA<22>
121	1 121	XRE SPO RD DATA<23>
122	1 122	XRE SPO RD DATA<28>
123	1 123	XRE SPO RD DATA<30>
124	1 124	XRE SPO RD DATA<31>
125	1 185	
126	1 186	
127	1 187	
128	1 188	
129	1 189	
130	1 190	
131	1 181	
132	1 182	
133	1 183	
134	1 184	
135	1 185	
136	1 186	
137	1 187	VPO SPO SPARE3
138	1 188	VPO SPO SPARE4
139	1 189	VPO SPO SPARE5
140	1 190	VPO SPO SPARE6

201	1 201	SPO XSO WR DATA<5>
202	1 202	SPO XSO WR DATA<6>
203	1 203	SPO XSO WR DATA<7>
204	1 204	SPO XSO WR DATA<13>
205	1 205	SPO XSO WR DATA<14>
206	1 206	SPO XSO WR DATA<15>
207	1 207	SPO XSO WR DATA<21>
208	1 208	SPO XSO WR DATA<22>
209	1 209	SPO XSO WR DATA<23>
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212	1 212	SPO XSO WR DATA<31>
213	1 213	SPO XSE WR DATA<5>
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216	1 216	SPO XSE WR DATA<13>
217	1 217	SPO XSE WR DATA<14>
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223	1 223	SPO XSE WR DATA<30>
224	1 224	SPO XSE WR DATA<31>
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232	1 212	
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239	1 219	
240	1 220	

301	1 301	VPO SPO DATA<5>
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303	1 303	VPO SPO DATA<7>
304	1 304	VPO SPO DATA<13>
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306	1 306	VPO SPO DATA<15>
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319	1 319	VPO SPO DATA<53>
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324	1 324	VPO SPO DATA<63>
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326	1 306	
327	1 307	
328	1 308	
329	1 309	
330	1 310	VPO SPO MIX REQ NEXT
331	1 311	VPO SPO SKV REQ NEXT
332	1 302	
333	1 303	VPO SPO PSW HAZ
334	1 304	VPO SPO SET FVZ
335	1 305	VPO SPO SET FSN
336	1 306	VPO SPO SET OV
337	1 307	VPO SPO SET RO
338	1 308	VPO SPO SET SDZ
339	1 309	VPO SPO SET SH
340	1 310	VPO SPO SET UN

401	1 401	SPO VPO DATA<5>
402	1 402	SPO VPO DATA<6>
403	1 403	SPO VPO DATA<7>
404	1 404	SPO VPO DATA<13>
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414	1 414	SPO VPO DATA<38>
415	1 415	SPO VPO DATA<39>
416	1 416	SPO VPO DATA<45>
417	1 417	SPO VPO DATA<46>
418	1 418	SPO VPO DATA<47>
419	1 419	SPO VPO DATA<53>
420	1 420	SPO VPO DATA<54>
421	1 421	SPO VPO DATA<55>
422	1 422	SPO VPO DATA<61>
423	1 423	SPO VPO DATA<62>
424	1 424	SPO VPO DATA<63>
425	1 405	
426	1 406	
427	1 407	
428	1 408	
429	1 409	
430	1 410	
431	1 411	
432	1 402	SPO VPO DISP RDY
433	1 403	SPO VPO DISP PSW HAZ
434	1 404	
435	1 405	
436	1 406	
437	1 407	
438	1 408	
439	1 409	
440	1 410	

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537	1 537	
538	1 538	
539	1 539	
540	1 540	GND/G

SECTION 2

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8 7 6 5 4 3 2 1

**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
SPO 77P
BOARD=NSP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=3

GROUND ALL PINS ON THIS ROW!

101	1 181	XRO SPO RD DATA<2>
102	1 182	XRO SPO RD DATA<3>
103	1 183	XRO SPO RD DATA<4>
104	1 184	XRO SPO RD DATA<10>
105	1 185	XRO SPO RD DATA<11>
106	1 186	XRO SPO RD DATA<12>
107	1 187	XRO SPO RD DATA<18>
108	1 188	XRO SPO RD DATA<19>
109	1 189	XRO SPO RD DATA<20>
110	1 118	XRO SPO RD DATA<26>
111	1 111	XRO SPO RD DATA<27>
112	1 112	XRO SPO RD DATA<28>
113	1 113	XRE SPO RD DATA<25>
114	1 114	XRE SPO RD DATA<32>
115	1 115	XRE SPO RD DATA<42>
116	1 116	XRE SPO RD DATA<10>
117	1 117	XRE SPO RD DATA<11>
118	1 118	XRE SPO RD DATA<12>
119	1 119	XRE SPO RD DATA<18>
120	1 120	XRE SPO RD DATA<19>
121	1 121	XRE SPO RD DATA<20>
122	1 122	XRE SPO RD DATA<26>
123	1 123	XRE SPO RD DATA<27>
124	1 124	XRE SPO RD DATA<28>
125	1 185	
126	1 186	
127	1 187	
128	1 188	
129	1 189	
130	1 190	
131	1 191	
132	1 129	VPO SPO VXA ADDR PAR<6>
133	1 129	VPO SPO VXA ADDR<6>
134	1 124	VPO SPO VXA ADDR<25>
135	1 125	VPO SPO VXA ADDR<26>
136	1 126	VPO SPO VXA ADDR<27>
137	1 127	VPO SPO VXA ADDR<28>
138	1 128	VPO SPO VXA ADDR<29>
139	1 129	VPO SPO VXA ADDR<30>
140	1 148	VPO SPO VXA ADDR<31>

201	1 201	SPO XSO WR DATA<2>
202	1 202	SPO XSO WR DATA<3>
203	1 203	SPO XSO WR DATA<4>
204	1 204	SPO XSO WR DATA<10>
205	1 205	SPO XSO WR DATA<11>
206	1 206	SPO XSO WR DATA<12>
207	1 207	SPO XSO WR DATA<18>
208	1 208	SPO XSO WR DATA<19>
209	1 209	SPO XSO WR DATA<20>
210	1 210	SPO XSO WR DATA<26>
211	1 211	SPO XSO WR DATA<27>
212	1 212	SPO XSO WR DATA<28>
213	1 213	SPO XSE WR DATA<25>
214	1 214	SPO XSE WR DATA<32>
215	1 215	SPO XSE WR DATA<42>
216	1 216	SPO XSE WR DATA<10>
217	1 217	SPO XSE WR DATA<11>
218	1 218	SPO XSE WR DATA<12>
219	1 219	SPO XSE WR DATA<18>
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223	1 223	SPO XSE WR DATA<27>
224	1 224	SPO XSE WR DATA<28>
225	1 205	
226	1 206	
227	1 207	
228	1 208	
229	1 209	
230	1 200	
231	1 201	
232	1 229	VPO SPO VXA ADDR PAR<1>
233	1 229	VPO SPO VXA ADDR<1>
234	1 224	VPO SPO VXA ADDR<17>
235	1 225	VPO SPO VXA ADDR<18>
236	1 226	VPO SPO VXA ADDR<19>
237	1 227	VPO SPO VXA ADDR<20>
238	1 228	VPO SPO VXA ADDR<21>
239	1 229	VPO SPO VXA ADDR<22>
240	1 248	VPO SPO VXA ADDR<23>

301	1 301	VPO SPO DATA<2>
302	1 302	VPO SPO DATA<3>
303	1 303	VPO SPO DATA<4>
304	1 304	VPO SPO DATA<10>
305	1 305	VPO SPO DATA<11>
306	1 306	VPO SPO DATA<12>
307	1 307	VPO SPO DATA<18>
308	1 308	VPO SPO DATA<19>
309	1 309	VPO SPO DATA<20>
310	1 310	VPO SPO DATA<26>
311	1 311	VPO SPO DATA<27>
312	1 312	VPO SPO DATA<28>
313	1 313	VPO SPO DATA<34>
314	1 314	VPO SPO DATA<35>
315	1 315	VPO SPO DATA<36>
316	1 316	VPO SPO DATA<42>
317	1 317	VPO SPO DATA<43>
318	1 318	VPO SPO DATA<44>
319	1 319	VPO SPO DATA<45>
320	1 320	VPO SPO DATA<51>
321	1 321	VPO SPO DATA<52>
322	1 322	VPO SPO DATA<53>
323	1 323	VPO SPO DATA<54>
324	1 324	VPO SPO DATA<60>
325	1 305	
326	1 306	
327	1 307	
328	1 308	
329	1 309	
330	1 300	
331	1 301	
332	1 329	VPO SPO VXA ADDR<16>
333	1 329	VPO SPO VXA ADDR<16>
334	1 304	
335	1 305	
336	1 306	
337	1 307	
338	1 308	
339	1 309	
340	1 348	VPO SPO VXA ADDR<24>

401	1 401	SPO VPO DATA<2>
402	1 402	SPO VPO DATA<3>
403	1 403	SPO VPO DATA<4>
404	1 404	SPO VPO DATA<10>
405	1 405	SPO VPO DATA<11>
406	1 406	SPO VPO DATA<12>
407	1 407	SPO VPO DATA<18>
408	1 408	SPO VPO DATA<19>
409	1 409	SPO VPO DATA<20>
410	1 410	SPO VPO DATA<26>
411	1 411	SPO VPO DATA<27>
412	1 412	SPO VPO DATA<28>
413	1 413	SPO VPO DATA<34>
414	1 414	SPO VPO DATA<35>
415	1 415	SPO VPO DATA<36>
416	1 416	SPO VPO DATA<42>
417	1 417	SPO VPO DATA<43>
418	1 418	SPO VPO DATA<44>
419	1 419	SPO VPO DATA<45>
420	1 420	SPO VPO DATA<51>
421	1 421	SPO VPO DATA<52>
422	1 422	SPO VPO DATA<53>
423	1 423	SPO VPO DATA<54>
424	1 424	SPO VPO DATA<60>
425	1 405	
426	1 406	
427	1 407	
428	1 408	
429	1 409	
430	1 400	
431	1 401	
432	1 402	
433	1 403	
434	1 404	
435	1 405	
436	1 406	
437	1 407	
438	1 408	
439	1 438	SPO VPO CNTX CTL<1>
440	1 448	SPO VPO CNTX CTL<2>

501	1 501	
502	1 502	
503	1 503	
504	1 504	
505	1 505	
506	1 506	
507	1 507	
508	1 508	
509	1 509	
510	1 510	
511	1 511	
512	1 512	
513	1 513	
514	1 514	
515	1 515	
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531	1 531	
532	1 532	
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539	1 539	
540	1 548	GN'D/G

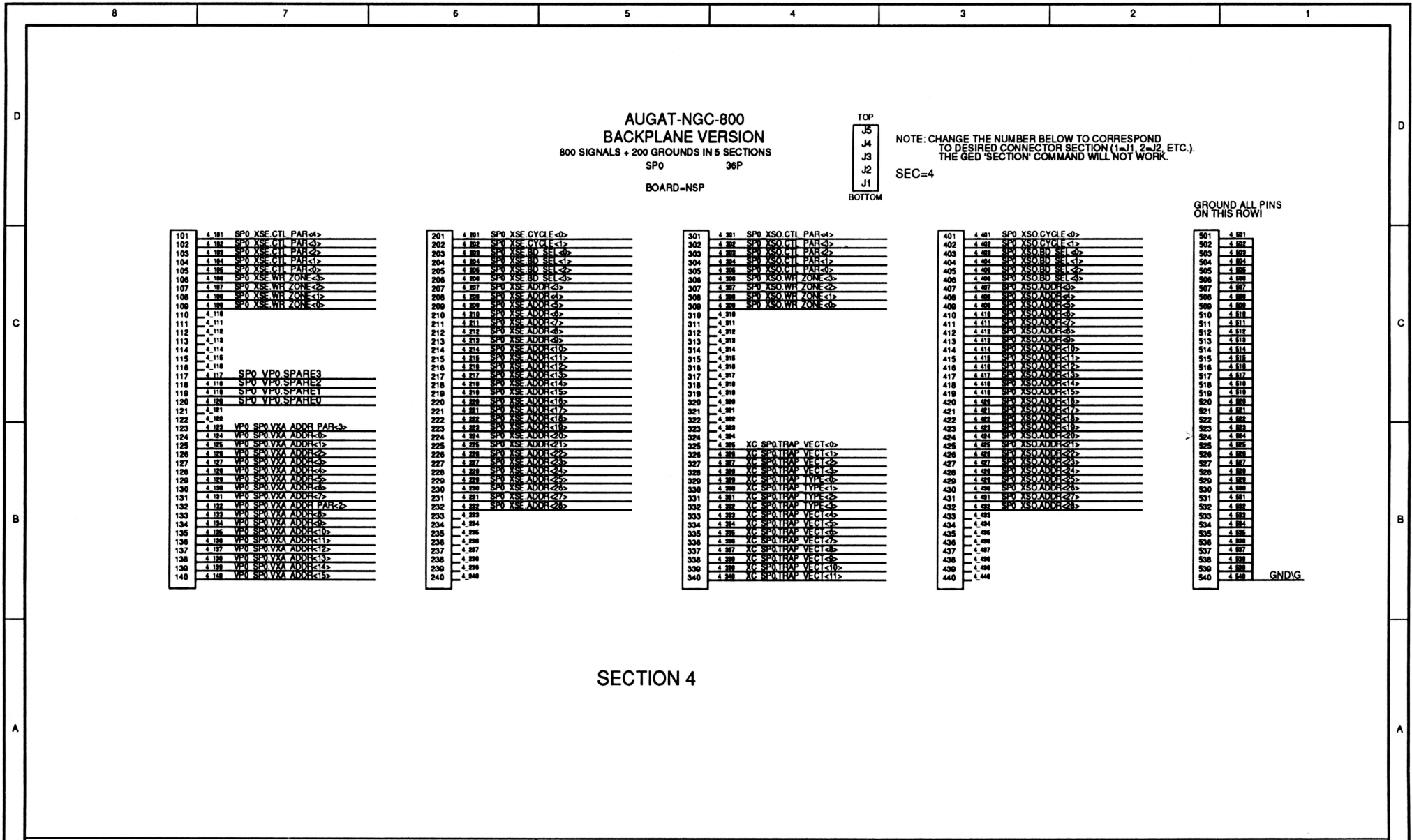
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REVISD: Tue May 21 09:03:01 1991
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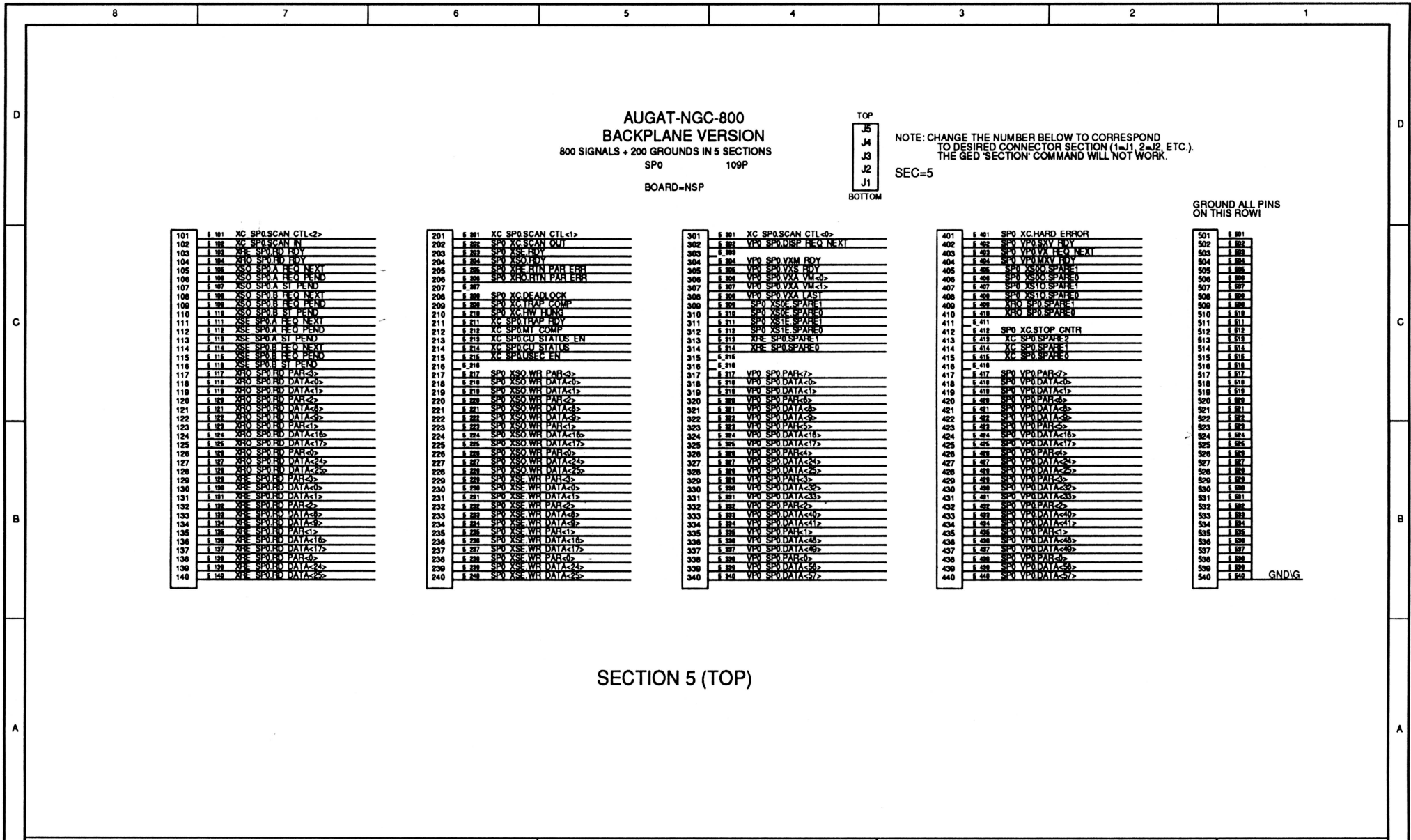
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SECTION 4



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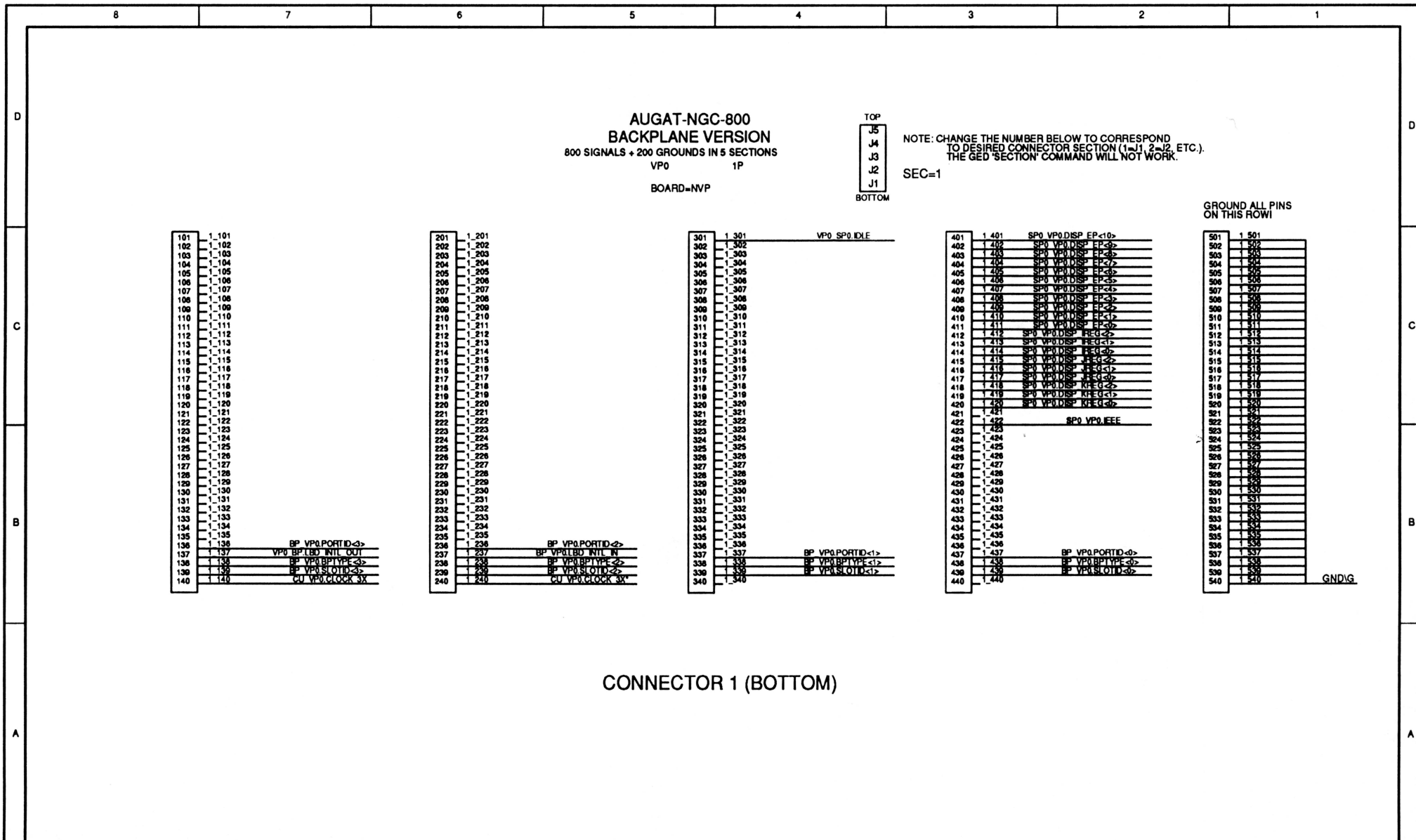


SECTION 5 (TOP)



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CONNECTOR 1 (BOTTOM)

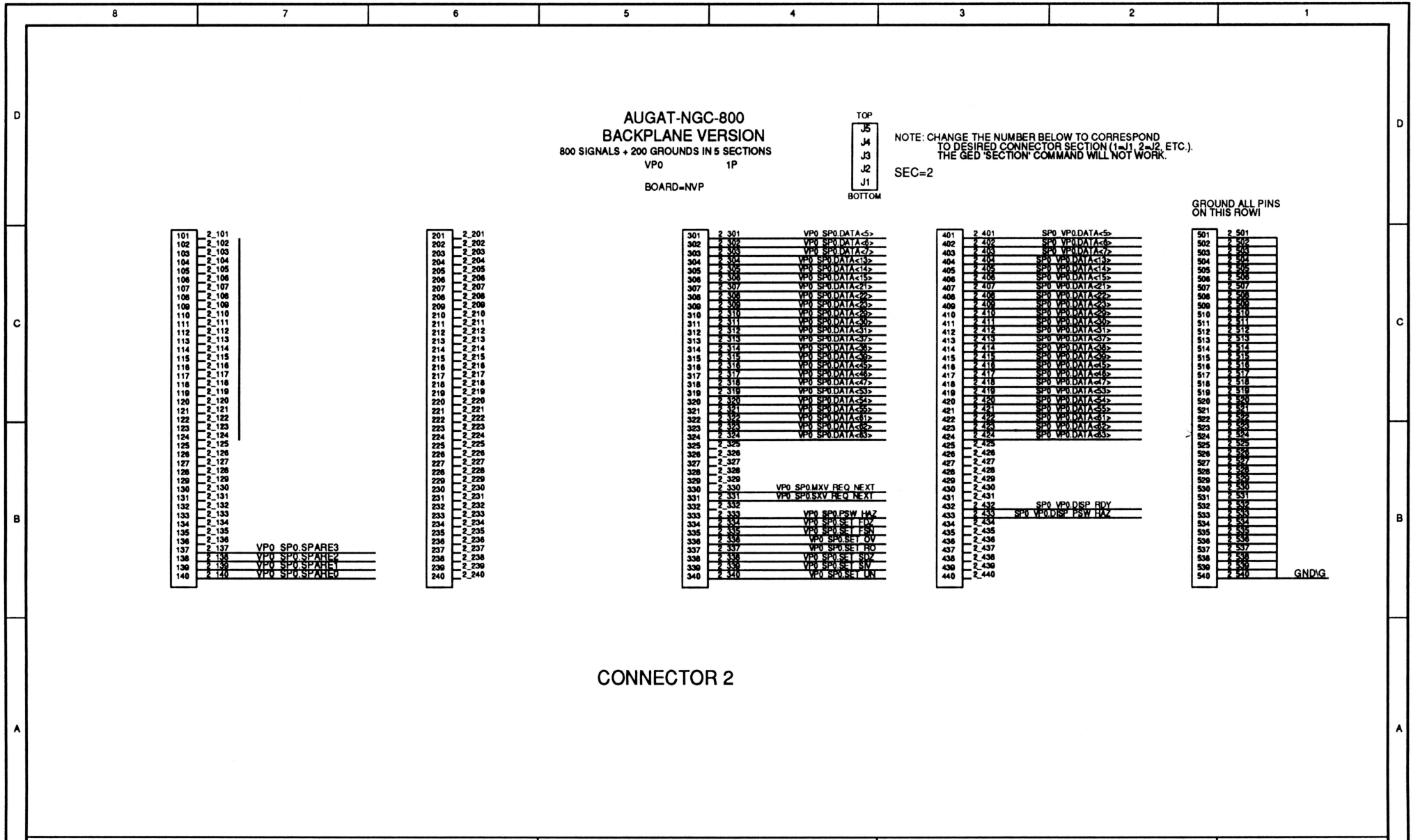
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**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
VPO 1P
BOARD=NVP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=2

GROUND ALL PINS ON THIS ROW

101	2_101
102	2_102
103	2_103
104	2_104
105	2_105
106	2_106
107	2_107
108	2_108
109	2_109
110	2_110
111	2_111
112	2_112
113	2_113
114	2_114
115	2_115
116	2_116
117	2_117
118	2_118
119	2_119
120	2_120
121	2_121
122	2_122
123	2_123
124	2_124
125	2_125
126	2_126
127	2_127
128	2_128
129	2_129
130	2_130
131	2_131
132	2_132
133	2_133
134	2_134
135	2_135
136	2_136
137	2_137
138	2_138
139	2_139
140	2_140

VPO SPO SPARE3
VPO SPO SPARE2
VPO SPO SPARE1
VPO SPO SPARE0

201	2_201
202	2_202
203	2_203
204	2_204
205	2_205
206	2_206
207	2_207
208	2_208
209	2_209
210	2_210
211	2_211
212	2_212
213	2_213
214	2_214
215	2_215
216	2_216
217	2_217
218	2_218
219	2_219
220	2_220
221	2_221
222	2_222
223	2_223
224	2_224
225	2_225
226	2_226
227	2_227
228	2_228
229	2_229
230	2_230
231	2_231
232	2_232
233	2_233
234	2_234
235	2_235
236	2_236
237	2_237
238	2_238
239	2_239
240	2_240

301	2_301	VPO SPO DATA<5>
302	2_302	VPO SPO DATA<6>
303	2_303	VPO SPO DATA<7>
304	2_304	VPO SPO DATA<13>
305	2_305	VPO SPO DATA<14>
306	2_306	VPO SPO DATA<15>
307	2_307	VPO SPO DATA<21>
308	2_308	VPO SPO DATA<22>
309	2_309	VPO SPO DATA<23>
310	2_310	VPO SPO DATA<29>
311	2_311	VPO SPO DATA<30>
312	2_312	VPO SPO DATA<31>
313	2_313	VPO SPO DATA<37>
314	2_314	VPO SPO DATA<38>
315	2_315	VPO SPO DATA<39>
316	2_316	VPO SPO DATA<45>
317	2_317	VPO SPO DATA<46>
318	2_318	VPO SPO DATA<47>
319	2_319	VPO SPO DATA<53>
320	2_320	VPO SPO DATA<54>
321	2_321	VPO SPO DATA<55>
322	2_322	VPO SPO DATA<61>
323	2_323	VPO SPO DATA<62>
324	2_324	VPO SPO DATA<63>
325	2_325	
326	2_326	
327	2_327	
328	2_328	
329	2_329	
330	2_330	VPO SPO MXV REQ NEXT
331	2_331	VPO SPO SXV REQ NEXT
332	2_332	
333	2_333	VPO SPO PSW HAZ
334	2_334	VPO SPO SET F0Z
335	2_335	VPO SPO SET F0N
336	2_336	VPO SPO SET OV
337	2_337	VPO SPO SET NOZ
338	2_338	VPO SPO SET NOZ
339	2_339	VPO SPO SET SV
340	2_340	VPO SPO SET UN

401	2_401	SPO VPO DATA<5>
402	2_402	SPO VPO DATA<6>
403	2_403	SPO VPO DATA<7>
404	2_404	SPO VPO DATA<13>
405	2_405	SPO VPO DATA<14>
406	2_406	SPO VPO DATA<15>
407	2_407	SPO VPO DATA<21>
408	2_408	SPO VPO DATA<22>
409	2_409	SPO VPO DATA<23>
410	2_410	SPO VPO DATA<29>
411	2_411	SPO VPO DATA<30>
412	2_412	SPO VPO DATA<31>
413	2_413	SPO VPO DATA<37>
414	2_414	SPO VPO DATA<38>
415	2_415	SPO VPO DATA<39>
416	2_416	SPO VPO DATA<45>
417	2_417	SPO VPO DATA<46>
418	2_418	SPO VPO DATA<47>
419	2_419	SPO VPO DATA<53>
420	2_420	SPO VPO DATA<54>
421	2_421	SPO VPO DATA<55>
422	2_422	SPO VPO DATA<61>
423	2_423	SPO VPO DATA<62>
424	2_424	SPO VPO DATA<63>
425	2_425	
426	2_426	
427	2_427	
428	2_428	
429	2_429	
430	2_430	
431	2_431	
432	2_432	SPO VPO DISP RDY
433	2_433	SPO VPO DISP PSW HAZ
434	2_434	
435	2_435	
436	2_436	
437	2_437	
438	2_438	
439	2_439	
440	2_440	

501	2_501
502	2_502
503	2_503
504	2_504
505	2_505
506	2_506
507	2_507
508	2_508
509	2_509
510	2_510
511	2_511
512	2_512
513	2_513
514	2_514
515	2_515
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520	2_520
521	2_521
522	2_522
523	2_523
524	2_524
525	2_525
526	2_526
527	2_527
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529	2_529
530	2_530
531	2_531
532	2_532
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540	2_540

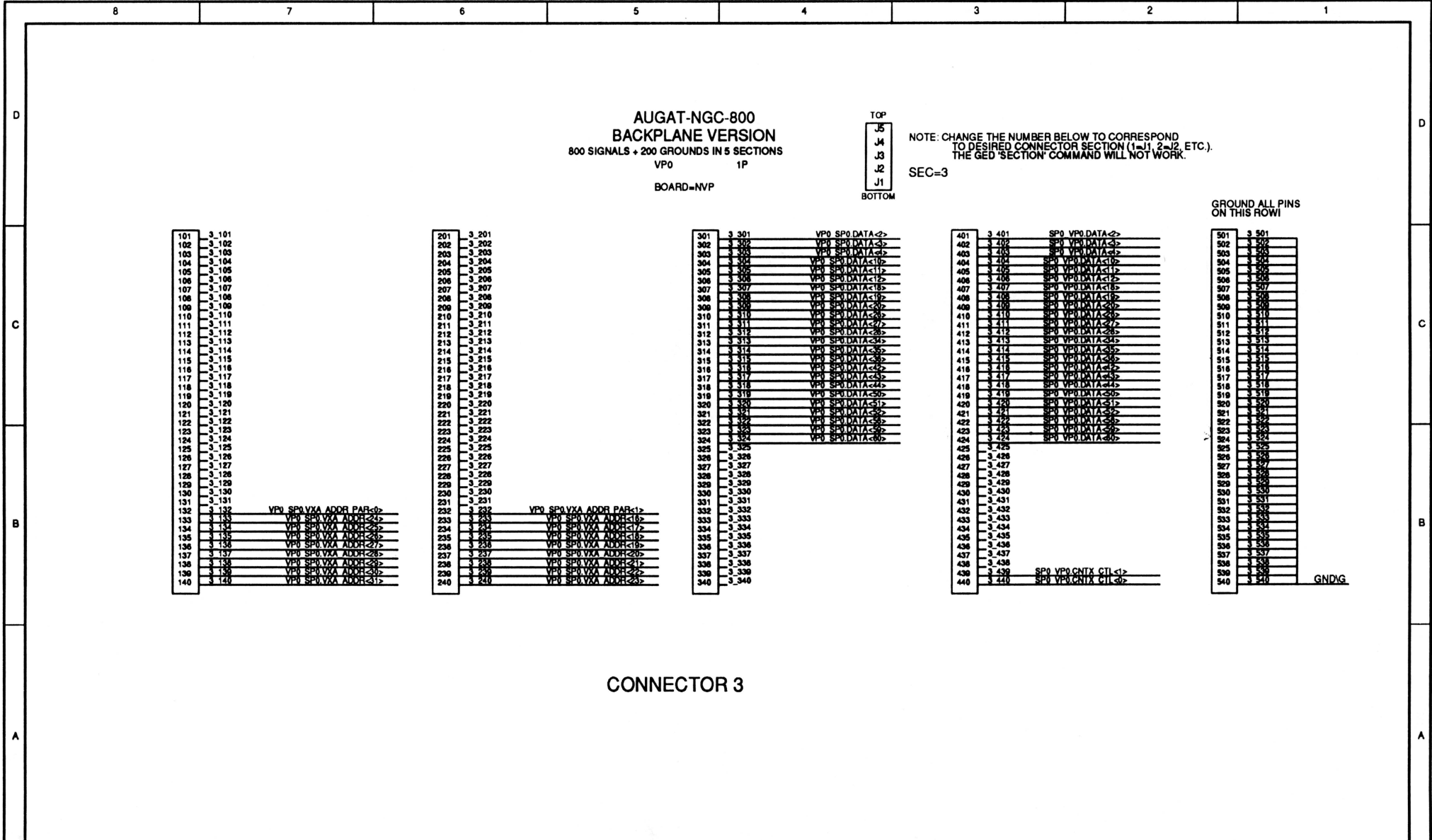
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CONNECTOR 2

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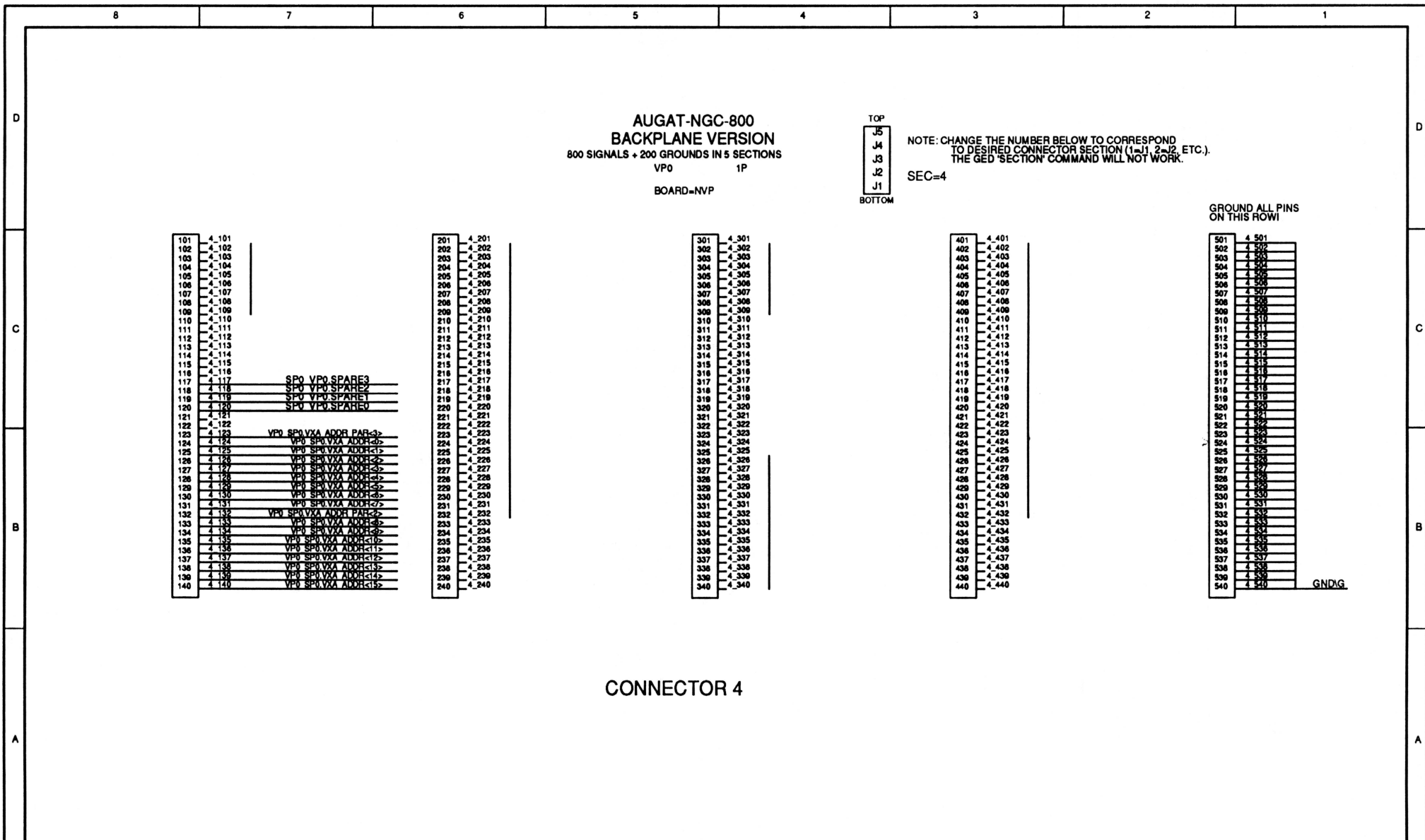


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3	2
	1



**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
VP0 1P
BOARD=NVP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=4

GROUND ALL PINS ON THIS ROW!

101	4_101	
102	4_102	
103	4_103	
104	4_104	
105	4_105	
106	4_106	
107	4_107	
108	4_108	
109	4_109	
110	4_110	
111	4_111	
112	4_112	
113	4_113	
114	4_114	
115	4_115	
116	4_116	
117	4_117	SPO VP0 SPARE3
118	4_118	SPO VP0 SPARE2
119	4_119	SPO VP0 SPARE1
120	4_120	SPO VP0 SPARE0
121	4_121	
122	4_122	
123	4_123	VP0 SPO VXA ADDR PAR-3
124	4_124	VP0 SPO VXA ADDR-6
125	4_125	VP0 SPO VXA ADDR-15
126	4_126	VP0 SPO VXA ADDR-2
127	4_127	VP0 SPO VXA ADDR-9
128	4_128	VP0 SPO VXA ADDR-18
129	4_129	VP0 SPO VXA ADDR-27
130	4_130	VP0 SPO VXA ADDR-36
131	4_131	VP0 SPO VXA ADDR-45
132	4_132	VP0 SPO VXA ADDR PAR-2
133	4_133	VP0 SPO VXA ADDR-6
134	4_134	VP0 SPO VXA ADDR-15
135	4_135	VP0 SPO VXA ADDR-24
136	4_136	VP0 SPO VXA ADDR-33
137	4_137	VP0 SPO VXA ADDR-42
138	4_138	VP0 SPO VXA ADDR-51
139	4_139	VP0 SPO VXA ADDR-60
140	4_140	VP0 SPO VXA ADDR-69

201	4_201	
202	4_202	
203	4_203	
204	4_204	
205	4_205	
206	4_206	
207	4_207	
208	4_208	
209	4_209	
210	4_210	
211	4_211	
212	4_212	
213	4_213	
214	4_214	
215	4_215	
216	4_216	
217	4_217	
218	4_218	
219	4_219	
220	4_220	
221	4_221	
222	4_222	
223	4_223	
224	4_224	
225	4_225	
226	4_226	
227	4_227	
228	4_228	
229	4_229	
230	4_230	
231	4_231	
232	4_232	
233	4_233	
234	4_234	
235	4_235	
236	4_236	
237	4_237	
238	4_238	
239	4_239	
240	4_240	

301	4_301	
302	4_302	
303	4_303	
304	4_304	
305	4_305	
306	4_306	
307	4_307	
308	4_308	
309	4_309	
310	4_310	
311	4_311	
312	4_312	
313	4_313	
314	4_314	
315	4_315	
316	4_316	
317	4_317	
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319	4_319	
320	4_320	
321	4_321	
322	4_322	
323	4_323	
324	4_324	
325	4_325	
326	4_326	
327	4_327	
328	4_328	
329	4_329	
330	4_330	
331	4_331	
332	4_332	
333	4_333	
334	4_334	
335	4_335	
336	4_336	
337	4_337	
338	4_338	
339	4_339	
340	4_340	

401	4_401	
402	4_402	
403	4_403	
404	4_404	
405	4_405	
406	4_406	
407	4_407	
408	4_408	
409	4_409	
410	4_410	
411	4_411	
412	4_412	
413	4_413	
414	4_414	
415	4_415	
416	4_416	
417	4_417	
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419	4_419	
420	4_420	
421	4_421	
422	4_422	
423	4_423	
424	4_424	
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432	4_432	
433	4_433	
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436	4_436	
437	4_437	
438	4_438	
439	4_439	
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501	4_501	
502	4_502	
503	4_503	
504	4_504	
505	4_505	
506	4_506	
507	4_507	
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523	4_523	
524	4_524	
525	4_525	
526	4_526	
527	4_527	
528	4_528	
529	4_529	
530	4_530	
531	4_531	
532	4_532	
533	4_533	
534	4_534	
535	4_535	
536	4_536	
537	4_537	
538	4_538	
539	4_539	
540	4_540	

GND/G

CONNECTOR 4

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ABBR: CBPL
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8 7 6 5 4 3 2 1

**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
VP0 1P
BOARD=NVP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=5

GROUND ALL PINS ON THIS ROW!

101	5 101	XC VP0.SCAN CTL<2>
102	5 102	XC VP0.SCAN IN
103	5 103	
104	5 104	
105	5 105	
106	5 106	
107	5 107	
108	5 108	
109	5 109	
110	5 110	
111	5 111	
112	5 112	
113	5 113	
114	5 114	
115	5 115	
116	5 116	
117	5 117	
118	5 118	
119	5 119	
120	5 120	
121	5 121	
122	5 122	
123	5 123	
124	5 124	
125	5 125	
126	5 126	
127	5 127	
128	5 128	
129	5 129	
130	5 130	
131	5 131	
132	5 132	
133	5 133	
134	5 134	
135	5 135	
136	5 136	
137	5 137	
138	5 138	
139	5 139	
140	5 140	

201	5 201	XC VP0.SCAN CTL<1>
202	5 202	VP0 XC.SCAN OUT
203	5 203	
204	5 204	
205	5 205	
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212	5 212	
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232	5 232	
233	5 233	
234	5 234	
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236	5 236	
237	5 237	
238	5 238	
239	5 239	
240	5 240	

301	5 301	XC VP0.SCAN CTL<0>
302	5 302	VP0 SP0.DSP REQ NEXT
303	5 303	
304	5 304	VP0 SP0.VM0 RDY
305	5 305	VP0 SP0.VX0 RDY
306	5 306	VP0 SP0.VXA VM<0>
307	5 307	VP0 SP0.VXA VM<1>
308	5 308	VP0 SP0.VXA LAST
309	5 309	
310	5 310	
311	5 311	
312	5 312	
313	5 313	
314	5 314	
315	5 315	
316	5 316	
317	5 317	VP0 SP0.PAR<2>
318	5 318	VP0 SP0.DATA<0>
319	5 319	VP0 SP0.DATA<1>
320	5 320	VP0 SP0.PAR<0>
321	5 321	VP0 SP0.DATA<0>
322	5 322	VP0 SP0.DATA<0>
323	5 323	VP0 SP0.PAR<0>
324	5 324	VP0 SP0.DATA<16>
325	5 325	VP0 SP0.DATA<17>
326	5 326	VP0 SP0.PAR<0>
327	5 327	VP0 SP0.DATA<0>
328	5 328	VP0 SP0.DATA<0>
329	5 329	VP0 SP0.PAR<0>
330	5 330	VP0 SP0.DATA<32>
331	5 331	VP0 SP0.DATA<33>
332	5 332	VP0 SP0.PAR<0>
333	5 333	VP0 SP0.DATA<0>
334	5 334	VP0 SP0.DATA<17>
335	5 335	VP0 SP0.PAR<1>
336	5 336	VP0 SP0.DATA<0>
337	5 337	VP0 SP0.DATA<0>
338	5 338	VP0 SP0.PAR<0>
339	5 339	VP0 SP0.DATA<0>
340	5 340	VP0 SP0.DATA<37>

401	5 401	VP0 XC.HARD ERROR
402	5 402	SP0 VP0.SXV RDY
403	5 403	SP0 VP0.VX REQ NEXT
404	5 404	SP0 VP0.MAV RDY
405	5 405	
406	5 406	
407	5 407	
408	5 408	
409	5 409	
410	5 410	
411	5 411	
412	5 412	
413	5 413	
414	5 414	
415	5 415	
416	5 416	
417	5 417	SP0 VP0.PAR<2>
418	5 418	SP0 VP0.DATA<0>
419	5 419	SP0 VP0.DATA<1>
420	5 420	SP0 VP0.PAR<0>
421	5 421	SP0 VP0.DATA<0>
422	5 422	SP0 VP0.DATA<0>
423	5 423	SP0 VP0.PAR<0>
424	5 424	SP0 VP0.DATA<16>
425	5 425	SP0 VP0.DATA<17>
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428	5 428	SP0 VP0.DATA<0>
429	5 429	SP0 VP0.PAR<0>
430	5 430	SP0 VP0.DATA<32>
431	5 431	SP0 VP0.DATA<33>
432	5 432	SP0 VP0.PAR<0>
433	5 433	SP0 VP0.DATA<0>
434	5 434	SP0 VP0.DATA<17>
435	5 435	SP0 VP0.PAR<1>
436	5 436	SP0 VP0.DATA<0>
437	5 437	SP0 VP0.DATA<0>
438	5 438	SP0 VP0.PAR<0>
439	5 439	SP0 VP0.DATA<0>
440	5 440	SP0 VP0.DATA<37>

501	5 501	
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532	5 532	
533	5 533	
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535	5 535	
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537	5 537	
538	5 538	
539	5 539	
540	5 540	GND/G

CONNECTOR 5 (TOP)

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8 7 6 3 2 1



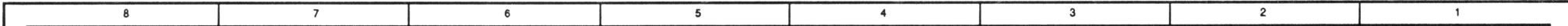
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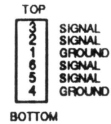
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ENGR: GOLEMBESKI

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DUPONT-204
CPU OR I/O BAY BACKPLANE TYPE
J02 1P



GROUND ALL PINS ON THESE TWO ROWS!

201	XRF SP0 RD DATA<28>
202	XRF SP0 RD DATA<27>
203	XRF SP0 RD DATA<26>
204	XRF SP0 RD DATA<25>
205	XRF SP0 RD DATA<19>
206	XRF SP0 RD DATA<18>
207	XRF SP0 RD DATA<31>
208	XRF SP0 RD DATA<30>
209	SPO ASE ADDR<21>
210	SPO ASE ADDR<20>
211	SPO ASE ADDR<19>
212	SPO ASE ADDR<18>
213	SPO ASE ADDR<17>
214	SPO ASE ADDR<16>
215	SPO ASE ADDR<15>
216	SPO ASE WH DATA<25>
217	SPO ASE WH DATA<24>
218	SPO ASE WH DATA<17>
219	SPO ASE WH DATA<16>
220	SPO ASE WH DATA<23>
221	SPO ASE WH DATA<22>
222	SPO ASE WH DATA<26>
223	NC
224	NC
225	XC IACCU HBE<3>
226	XC IACCU HBE<2>
227	XC IACLOG ENA
228	XC IACCU CNTRL ENA
229	
230	XSE MBO WH DATA<25>
231	XSE MBO WH DATA<24>
232	XSE MBO WH DATA<17>
233	XSE MBO WH DATA<16>
234	XSE MBO WH DATA<28>
235	XSE MBO WH DATA<27>
236	XSE MBO WH DATA<26>
237	XSE MBO ADDR<21>
238	XSE MBO ADDR<20>
239	XSE MBO ADDR<19>
240	XSE MBO ADDR<18>
241	XSE MBO ADDR<17>
242	XSE MBO ADDR<16>
243	XSE MBO ADDR<15>
244	MBO XHO RD DATA<28>
245	MBO XHO RD DATA<27>
246	MBO XHO RD DATA<26>
247	MBO XHO RD DATA<25>
248	MBO XHO RD DATA<19>
249	MBO XHO RD DATA<18>
250	MBO XHO RD DATA<31>
251	MBO XHO RD DATA<30>

301	XRO SP0 RD DATA<28>
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314	SPO XSO ADDR<16>
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318	SPO XSO WH DATA<17>
319	SPO XSO WH DATA<16>
320	SPO XSO WH DATA<23>
321	SPO XSO WH DATA<22>
322	SPO XSO WH DATA<26>
323	NC
324	NC
325	XC IACCU HBE<1>
326	XC IACCU HBE<2>
327	XC IACCU CLEAR
328	XC IACLOCK SYNC
329	
330	XSO MBO WH DATA<25>
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332	XSO MBO WH DATA<17>
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335	XSO MBO WH DATA<27>
336	XSO MBO WH DATA<26>
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342	XSO MBO ADDR<16>
343	XSO MBO ADDR<15>
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347	MBO XHO RD DATA<25>
348	MBO XHO RD DATA<19>
349	MBO XHO RD DATA<18>
350	MBO XHO RD DATA<31>
351	MBO XHO RD DATA<30>

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502	XRF SP0 RD DATA<5>
503	XRF SP0 RD DATA<12>
504	XRF SP0 RD DATA<11>
505	XRF SP0 RD DATA<10>
506	XRF SP0 RD DATA<9>
507	XRF SP0 RD DATA<3>
508	XRF SP0 RD DATA<2>
509	XSE SPOB HEO NEXT
510	XSE SPOA HEO FEND
511	XSE SPOA ST FEND
512	SPO ASE WH DATA<10>
513	SPO ASE WH DATA<9>
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515	SPO ASE WH DATA<2>
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517	SPO ASE WH DATA<24>
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519	XC SPOUSEC EN
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525	XC SPO TRAP VECI<30>
526	XC SPO TRAP TYPE<32>
527	XC SPO TRAP TYPE<30>
528	SPO XC TRAP COMP
529	SPO XC DEADLOCK
530	XC SPO CU STATUS
531	XC MBO SCAN CIL<2>
532	XSE MBO RDY
533	XSE MBO HEP HEQ
534	
535	MBO ASE BANK DONE<15>
536	MBO ASE BANK DONE<14>
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539	MBO ASE BANK DONE<11>
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542	MBO ASE BANK DONE<8>
543	MBO ASE BANK DONE<7>
544	MBO ASE BANK DONE<6>
545	MBO ASE BANK DONE<5>
546	MBO ASE BANK DONE<4>
547	MBO ASE BANK DONE<3>
548	MBO ASE BANK DONE<2>
549	MBO ASE BANK DONE<1>
550	MBO ASE BANK DONE<0>
551	MBO XSO SENU PAH EHH

601	XRO SP0 RD DATA<6>
602	XRO SP0 RD DATA<5>
603	XRO SP0 RD DATA<12>
604	XRO SP0 RD DATA<11>
605	XRO SP0 RD DATA<10>
606	XRO SP0 RD DATA<9>
607	XRO SP0 RD DATA<3>
608	XRO SP0 RD DATA<2>
609	XSO SPOB HEO NEXT
610	XSO SPOA HEO FEND
611	XSO SPOA ST FEND
612	SPO XSO WH DATA<10>
613	SPO XSO WH DATA<9>
614	SPO XSO WH DATA<3>
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616	SPO XSO WH DATA<25>
617	SPO XSO WH DATA<24>
618	SPO XSO WH DATA<17>
619	XC SPO TRAP VECI<11>
620	XC SPO TRAP VECI<10>
621	XC SPO TRAP VECI<9>
622	XC SPO TRAP VECI<8>
623	XC SPO TRAP VECI<17>
624	XC SPO TRAP VECI<30>
625	VPO XC HARD ERROR
626	XC SPO TRAP TYPE<32>
627	XC SPO TRAP TYPE<30>
628	XC SPO TRAP RDY
629	XC SPO MI COMP
630	XC SPO CU STATUS EN
631	SPO XC HARD ERROR
632	XC SPO SCAN CIL<2>
633	XSO MBO RDY
634	XSO MBO HEP HEQ
635	MBO XSO BANK DONE<15>
636	MBO XSO BANK DONE<14>
637	MBO XSO BANK DONE<13>
638	MBO XSO BANK DONE<12>
639	MBO XSO BANK DONE<11>
640	MBO XSO BANK DONE<10>
641	MBO XSO BANK DONE<9>
642	MBO XSO BANK DONE<8>
643	MBO XSO BANK DONE<7>
644	MBO XSO BANK DONE<6>
645	MBO XSO BANK DONE<5>
646	MBO XSO BANK DONE<4>
647	MBO XSO BANK DONE<3>
648	MBO XSO BANK DONE<2>
649	MBO XSO BANK DONE<1>
650	MBO XSO BANK DONE<0>
651	MBO XSO SENU PAH EHH

101		401
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GND/G GND/G

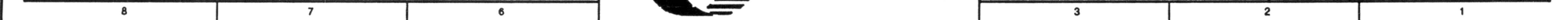
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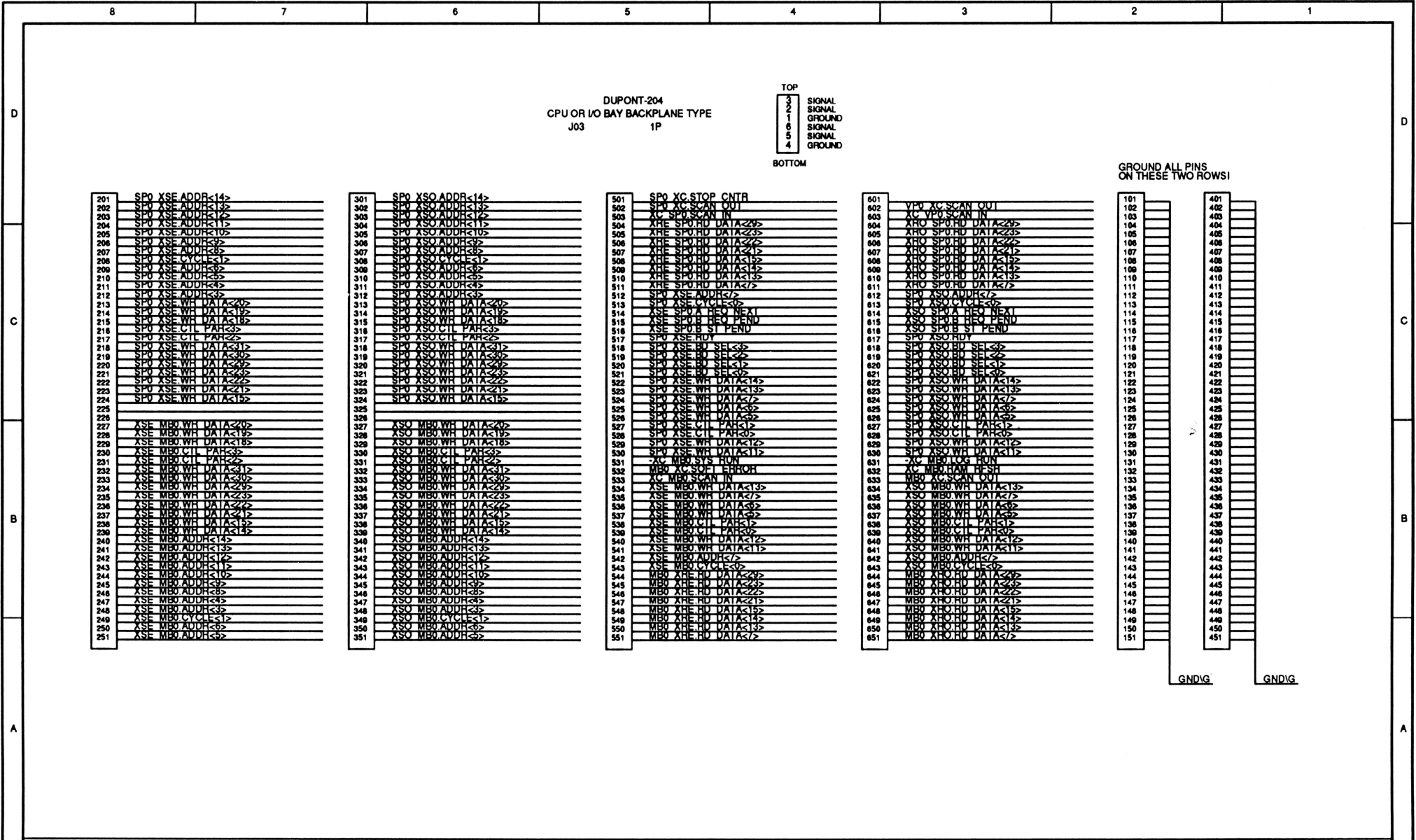


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DUPONT-204
CPU OR I/O BAY BACKPLANE TYPE
J03 1P

TOP
3 SIGNAL
2 SIGNAL
1 GROUND
6 SIGNAL
5 SIGNAL
4 GROUND
BOTTOM

GROUND ALL PINS
ON THESE TWO ROWS!

GND'G GND'G

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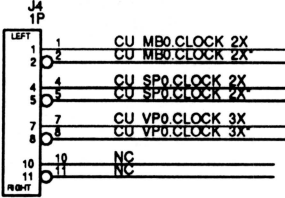


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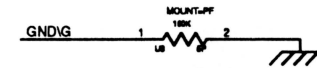
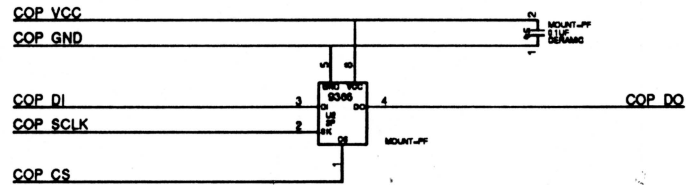
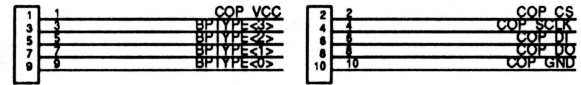
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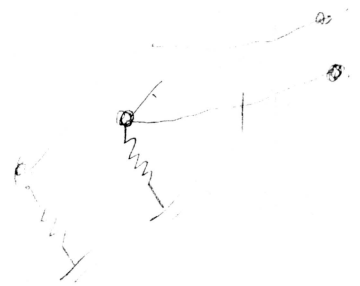
C3 CLOCK CONNECTOR
12 PIN STRAIGHT, 3X4 ROW
(& R VIEWED FROM INSIDE CARDGAGE)



HDR10
301-000035-004
MALE-PRESSFIT-STRAIGHT-4 WALL-LONG EJECTOR



RESISTOR CONNECTS CHASSIS GND TO SIGNAL GND



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SYNONYMS

BP BP.PORTID 0<3>	12P	BP MB0.PORTID<3>
BP BP.PORTID 0<2>	11P	BP MB0.PORTID<2>
BP BP.PORTID 0<1>	10P	BP MB0.PORTID<1>
BP BP.PORTID 0<0>	9P	BP MB0.PORTID<0>
BP BP.PORTID 0<3>	8P	BP SP0.PORTID<3>
BP BP.PORTID 0<2>	7P	BP SP0.PORTID<2>
BP BP.PORTID 0<1>	6P	BP SP0.PORTID<1>
BP BP.PORTID 0<0>	5P	BP SP0.PORTID<0>
BP BP.PORTID 0<3>	4P	BP VP0.PORTID<3>
BP BP.PORTID 0<2>	3P	BP VP0.PORTID<2>
BP BP.PORTID 0<1>	2P	BP VP0.PORTID<1>
BP BP.PORTID 0<0>	1P	BP VP0.PORTID<0>
MB0 BP.LBD INTL OUT	29P	BP MB0.LBD INTL IN
SP0 BP.LBD INTL OUT	30P	BP SP0.LBD INTL IN
VP0 BP.LBD INTL OUT	31P	BP VP0.LBD INTL IN

BPTYPE<3>	21P	COP GND
BPTYPE<2>	22P	COP GND
BPTYPE<1>	23P	NC
BPTYPE<0>	24P	COP GND
CPU LEFT BPTYPE = 0X2		
BP MB0.SLOTID<3>	25P	GNDVG
BP MB0.SLOTID<2>	26P	
BP MB0.SLOTID<1>	27P	
BP MB0.SLOTID<0>	28P	GNDVG
MB0 SLOTID = 0X6		
BP SP0.SLOTID<3>	17P	GNDVG
BP SP0.SLOTID<2>	18P	
BP SP0.SLOTID<1>	19P	
BP SP0.SLOTID<0>	20P	
SP0 SLOTID = 0X7		
BP VP0.SLOTID<3>	13P	
BP VP0.SLOTID<2>	14P	GNDVG
BP VP0.SLOTID<1>	15P	GNDVG
BP VP0.SLOTID<0>	16P	GNDVG
VP0 SLOTID = 0X8		

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